CLAIMS

1. A method for approximating power consumption of a circuit with a plurality of local clock buffers (LCBs), comprising:

inputting a Hardware Descriptive Language (HDL) simulator data of the circuit;

inputting net capacitance data of the circuit;

inputting energy model data, wherein the energy model data further comprises extrapolating energy data by increasing or decreasing the number of active LCBs; and

generating power consumption data from the HDL simulator data, the net capacitance data, and the energy model data.

2. The method of Claim 1, wherein each LCB of the plurality of LCBs is at least configured to consume the same amount of power.

3. The method of Claim 2, wherein the energy model data further comprises extrapolating energy data by increasing or decreasing numbers of active input signals to the circuit.

4. The method of Claim 1, wherein the method further comprises inputting template data, wherein the template data

is at least configured to contain relative power consumption data for each LCB of the plurality of LCBs.

5. The method of Claim 4, wherein the relative power consumption data of each LCB of the plurality of LCBs are at least configured to be the same or different.

6. The method of Claim 5, wherein each LCB of the plurality of LCBs is at least configured to consume the same amount of power.

7. The method of Claim 6, wherein the energy model data further comprises extrapolating energy data by increasing or decreasing numbers of active input signals to the circuit.

8. The method of Claim 7, wherein the generating power consumption data is at least configured to utilize the template data.

9. An apparatus for approximating power consumption of a circuit with a plurality of local clock buffers (LCBs), comprising:

means for inputting HDL simulator data of the circuit;

means for inputting net capacitance data of the circuit;

means for inputting energy model data, wherein the energy model data further comprises extrapolating energy data by increasing or decreasing the number of active LCBs; and

means for generating power consumption data from the HDL simulator data, the net capacitance data, and the energy model data.

10. The apparatus of Claim 9, wherein each LCB of the plurality of LCBs is at least configured to consume the same amount of power.

11. The apparatus of Claim 10, wherein the energy model data further comprises means for extrapolating energy data by increasing or decreasing numbers of active input signals to the circuit.

12. The apparatus of Claim 9, wherein the method further comprises means for inputting template data, wherein the template data is at least configured to contain relative power consumption data for each LCB of the plurality of LCBs.

13. The apparatus of Claim 12, wherein the relative power consumption data of each LCB of the plurality of LCBs are at least configured to be the same or different.

14. The method of Claim 13, wherein each LCB of the plurality of LCBs is at least configured to consume the same amount of power.

15. The method of Claim 14, wherein the energy model data further comprises means for extrapolating energy data by increasing or decreasing numbers of active input signals to the circuit.

16. The method of Claim 15, wherein the means for generating power consumption data is at least configured to utilize the template data.

17. A computer program product for approximating power consumption of a circuit with a plurality of local clock buffers (LCBs), the computer program product having a medium with a computer program embodied thereon, the computer program comprising:

computer code for inputting HDL simulator data of the circuit;

computer code for inputting net capacitance data of the circuit;

computer code for inputting energy model data, wherein the energy model data further comprises extrapolating energy data by increasing or decreasing the number of active LCBs; and

computer code for generating power consumption data from the HDL simulator data, the net capacitance data, and the energy model data.

18. The computer program product of Claim 17, wherein each LCB of the plurality of LCBs is at least configured to consume the same amount of power.

19. The computer program product of Claim 18, wherein the energy model data further comprises computer code for extrapolating energy data by increasing or decreasing numbers of active input signals to the circuit.

20. The computer program product of Claim 17, wherein the method further comprises computer code for inputting template data, wherein the template data is at least configured to contain relative power consumption data for each LCB of the plurality of LCBs.

21. The computer program product of Claim 20, wherein the relative power consumption data of each LCB of the plurality of LCBs are at least configured to be the same or different.

22. The computer program product of Claim 21, wherein each LCB of the plurality of LCBs is at least configured to consume the same amount of power.

23. The computer program product of Claim 22, wherein the energy model data further comprises computer code for extrapolating energy data by increasing or decreasing numbers of active input signals to the circuit.

24. The computer program product of Claim 23, wherein the generating power consumption data is at least configured to utilize the template data.